**Link Module User Manual**

The module is made up of 6 memory modules and 4 registers.

* 2 output memory modules (data and config) and 1 counter for output memory.
* 2 input memory modules (data and config) and 1 counter for input memory.
* 1 Functional and reset register (Main register)
* 1 Delay register
* 1 static data register
* 1 static configuration register

Steps:

1. Desable the resets by writing 1EH at the main register
2. Start internal clocks by writing a default value at the Static configuration register
3. (Optional) write the static data register
4. (Optional) write de delay register – This value represents how many clock cycles will the input memory wait from the moment the main registrar is written with the 1FH value to start recording the inputs.
5. Write the data, configuration and output counter memory’s – the value at each location of the counter register represents how many clock cycles the data and configuration values will be kept on the bus. If the same data requires 2 configuration values, then 2 locations are required.
6. Start the output by writing 1FH to the main register.
7. When the value in the main register is turns 1EH, the input data is ready and may be read.
8. Read the data.